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IN RE APPLICATION OF

:

FUMITOMO MATSUOKA

: EXAMINER: TOLEDO, F. L.

SERIAL NO: 09/816,393

19/010,393

RCE FILED: JUNE 4, 2003

: GROUP ART UNIT: 2823

FOR: METHOD FOR MANUFACTURING:

A SEMICONDUCTOR DEVICE WITH A BURIED GATE ELECTRODE...

AMENDMENT

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

Responsive to the Official Action mailed on July 2, 2003, please amend the aboveidentified application as follows:

Amendments to the Claims are reflected in the listing of the claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.

0CT - 3 2003